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**In the Claims:****Claims 1-13 (canceled)****Claim 14 (currently amended):** A circuit arrangement comprising:

an active EMI filter including an input and an output, said input ~~connected~~  
configured to receive an output voltage from a power switching stage and said output of  
said active EMI filter configured to provide ~~providing~~ a filtered output voltage to a load;

a current sensor sensing a common mode current to said load;

an amplifier stage including first and second transistors each controlled by said  
current sensor, collectors of said first and second transistors connected together ~~sharing a~~  
~~common connection~~;

isolating circuitry coupled to said connected collectors of said first and second  
transistors ~~common connection~~ and a ground line, wherein said isolating circuitry passes a  
current to cancel said common mode current in said ground line.

**Claim 15 (previously presented):** The circuit arrangement of claim 14, wherein  
said first and second transistors are complementary.

**Claim 16 (previously presented):** The circuit arrangement of claim 14, wherein  
said current sensor comprises a current transformer including first and second primary

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windings and first and second secondary windings, said first and second secondary windings being connected to additively reflect said common mode current in said first and second secondary windings.

**Claim 17 (previously presented):** The circuit arrangement of claim 14, wherein said current sensor comprises a current transformer including first and second primary windings and first and second secondary windings, said first and said second primary windings coupled respectively to input terminals of said active EMI filter and said first and second secondary windings coupled respectively to said first and second transistors.

**Claim 18 (previously presented):** The circuit arrangement of claim 17, wherein said first and second transistors are complementary, only one of said complementary transistors conducting depending upon a direction of a current in said first and second secondary windings.

**Claim 19 (previously presented):** The circuit arrangement of claim 16, wherein one of said first and second transistors is turned ON to allow said reflected common mode current to flow through said isolating circuitry to cancel ground noise current flowing in said ground line, thereby canceling said ground noise current flowing back to said input.

**Claim 20 (previously presented):** The circuit arrangement of claim 14, wherein

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said power switching stage comprises an output stage including an inductor and a capacitor, said output voltage provided across said capacitor.

**Claim 21 (previously presented):** The circuit arrangement of claim 14, wherein said power switching stage comprises a switch mode power supply.

**Claim 22 (previously presented):** The circuit arrangement of claim 14, wherein said ground line connects said load and said power switching stage.

**Claim 23 (previously presented):** The circuit arrangement of claim 14, wherein said output voltage of said power switching stage comprises direct current.

**Claim 24 (previously presented):** The circuit arrangement of claim 14, wherein said output voltage of said power switching stage comprises alternating current.

**Claim 25 (currently amended):** A circuit arrangement comprising:  
an active EMI filter including first and second input terminals and first and second output terminals and a ground return line connected to a ground return line terminal;  
said input terminals of said active EMI filter ~~connected~~ configured to receive an output voltage of a power ~~transistor~~ switching stage over a voltage line and said output terminals of said active EMI filter providing a filtered output voltage;

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wherein said active EMI filter comprises a current transformer including first and second primary windings and first and second secondary windings, said first and second secondary windings being connected to ~~additively reflect said~~ have common mode current additively reflected in said first and second secondary windings to control an amplifier stage of said active EMI filter, said amplifier stage configured to not be coupled into said voltage line;

wherein said active EMI filter cancels said common mode current that flows between said input terminals and said output terminals.

**Claim 26 (currently amended):** The circuit arrangement of claim 25, wherein said power transistor switching stage comprises an output stage including an inductor and a capacitor, said output voltage provided across said capacitor.

**Claim 27 (currently amended):** The circuit arrangement of claim 25, said ~~active EMI filter comprising an~~ amplifier stage including first and second transistors each controlled by said current transformer, said first and second transistors sharing a common connection, said common connection coupled to isolating circuitry and said ground line, said isolating circuitry passing a current to cancel said common mode current in said ground line.

**Claim 28 (previously presented):** The circuit arrangement of claim 27, wherein

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said first and second transistors are complementary.

**Claim 29 (currently amended):** The circuit arrangement of claim 25, wherein said power transistor switching stage comprises a switch mode power supply.

**Claim 30 (previously presented):** The circuit arrangement of claim 25, wherein said active EMI filter comprises complementary transistors, only one of said complementary transistors conducting depending upon a direction of a current in said first and second secondary windings.

**Claim 31 (currently amended):** The circuit arrangement of claim 27 [[24]], comprising a power source coupled to the common connection of the first and second transistors wherein said ground line connects said load and said power transistor switching stage.

**Claim 32 (currently amended):** The circuit arrangement of claim 25, wherein said output voltage of said power transistor switching stage comprises direct current.

**Claim 33 (currently amended):** The circuit arrangement of claim 25, wherein said output voltage of said power transistor switching stage comprises alternating current.